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| Application Number     |                |
| Filing Date            | June 28, 2003  |
| First Named Inventor   | Chih-Hsin Wang |
| Art Unit               |                |
| Examiner Name          |                |
| Attorney Docket Number |                |

Sheet 1 of 1

**U. S. PATENT DOCUMENTS**

| Examiner Initials* | Cite No. <sup>1</sup> | Document Number<br>Number-Kind Code <sup>2</sup> (if known) | Publication Date<br>MM-DD-YYYY | Name of Patentee or<br>Applicant of Cited Document | Pages, Columns, Lines, Where<br>Relevant Passages or Relevant<br>Figures Appear |
|--------------------|-----------------------|---|--------------------------------|--|---|
| MH                 |                       | US- 5,780,341   | 07-14-1998                     | Seiki Ogura  | Figs. 4A, 4B, 6A, 6B  |
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| MH                 |                       | US- 4,957,877   | 09-18-1990                     | Simon M. Tam et al.                                | Figs. 5A, 5B  |
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| MH                 |                       | US- 5,432,739   | 07-11-1995                     | H. B. Pein   | c3 L57-65   |
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| MH                 |                       | US- 6,479,863 B2  | 11-12-2002                     | John Caywood                                       | Figs. 3b, 5b, 17d,  |
|                    |                       | US-   |                                |  | 17e, 18d  |
| MH                 |                       | US- 6,372,617 B1  | 04-16-2002                     | T. Kitamura  | Fig. 3A   |
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**FOREIGN PATENT DOCUMENTS**

| Examiner Initials* | Cite No. <sup>1</sup> | Foreign Patent Document<br>Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known) | Publication Date<br>MM-DD-YYYY | Name of Patentee or<br>Applicant of Cited Document | Pages, Columns, Lines,<br>Where Relevant Passages<br>Or Relevant Figures Appear | T <sup>6</sup> |
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First Named Inventor

Chih-Hsin Wang

Art Unit

Examiner Name

Attorney Docket Number

Sheet 1

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**NON PATENT LITERATURE DOCUMENTS**

| Examiner Initials* | Cite No. <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T <sup>2</sup> |
|--------------------|-----------------------|---|----------------|
| USA                |                       | Kitamura et al., "A Low Voltage Operating Flash Cell with High Coupling Ratio Using Horned Floating Gate with Fine HSG," Symposium on VLSI Technology, p. 104, 1998   |                |
| USA                |                       | Lai, "Flash Memories: Where We Were and Where We Are Going", IEDM Technical Digest, pp. 971-973, 1998.  |                |
| USA                |                       | Kuo et al., "TEFET - A High Density, Low Erase Voltage, Trench Flash EEPROM", Symposium on VLSI Technology, p. 51, 1994   |                |
| USA                |                       | Wang et al., "Three-Dimensional DIBL for Shallow-Trench Isolated MOSFET's", IEEE Trans. on Electron Devices, vol. 46, pp. 139-144, 1999   |                |
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